

Product Document



Application Note: AS3722– OTP Specification -13

AS3722-BCTT-13

AS3722-BWLT-13

OTP Specification -13

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Revision History

Revision	Date	Owner	Description
0.90	28.10.2014	pkm	Initial version
1.00	09.01.2015	pkm	Added NVPN and MPN

1 General Description

The "-13" OTP version is applicable for the following ordering codes:

Ordering code	MPN	Reel Size	NVPN
AS3722-BCTT-13	193600025	500pcs	315-0933-000

The silicon version used for these build used 1v21.

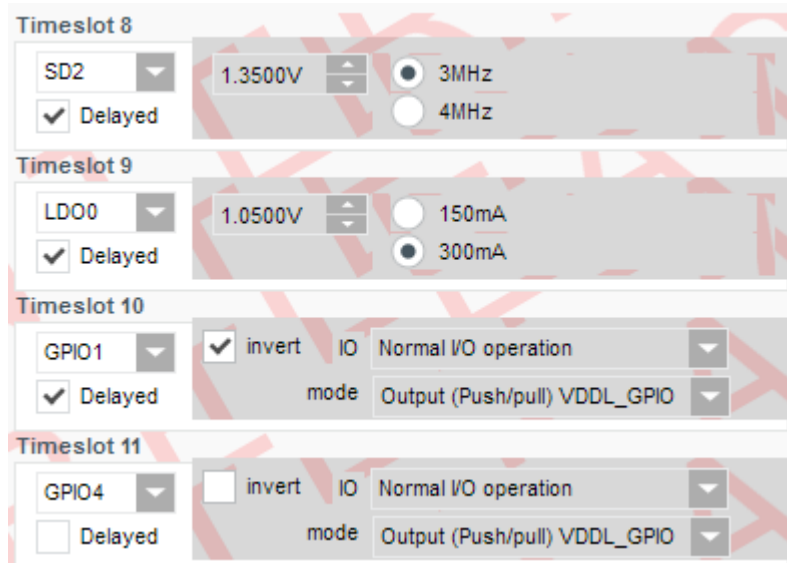
2 OTP Description

File name: AS3722_OTP-13_nVidia_1V35mem_1S_-200mV_20141001.txt

2.1 GUI SW screenshot



Timeslot 0	
LDO3 <input type="text"/>	1.1000V <input type="text"/> Mode PMOS LDO tracking <input type="text"/>
<input checked="" type="checkbox"/> Delayed	
Timeslot 1	
SD1 <input type="text"/>	1.1000V <input type="text"/> <input checked="" type="radio"/> Normal <input type="radio"/> Low Power
<input checked="" type="checkbox"/> Delayed	
Timeslot 2	
Not Used <input type="text"/>	
<input checked="" type="checkbox"/> Delayed	
Timeslot 3	
Not Used <input type="text"/>	
<input checked="" type="checkbox"/> Delayed	
Timeslot 4	
Not Used <input type="text"/>	
<input checked="" type="checkbox"/> Delayed	
Timeslot 5	
Not Used <input type="text"/>	
<input checked="" type="checkbox"/> Delayed	
Timeslot 6	
SD5 <input type="text"/>	1.8000V <input type="text"/> <input checked="" type="radio"/> 3MHz <input type="radio"/> 4MHz
<input checked="" type="checkbox"/> Delayed	
Timeslot 7	
GPIO2 <input type="text"/>	<input checked="" type="checkbox"/> invert IO Normal I/O operation <input type="text"/>
<input type="checkbox"/> Delayed	mode Output (Push/pull) VDDL_GPIO <input type="text"/>



2.2 Start-up file

Fuse Register Settings

Register 0xa7 = 0x38 (00111000b)
Register 0xa8 = 0x4f (01001111b)
Register 0xa9 = 0xec (11101100b)
Register 0xaa = 0x57 (01010111b)
Register 0xab = 0x7e (01111110b)
Register 0xac = 0x41 (01000001b)
Register 0xad = 0x00 (00000000b)
Register 0xae = 0x00 (00000000b)
Register 0xaf = 0x00 (00000000b)
Register 0xb0 = 0xfb (11111011b)
Register 0xb1 = 0x13 (00010011b)
Register 0xb2 = 0x59 (01011001b)
Register 0xb3 = 0x32 (00110010b)
Register 0xb4 = 0xcc (11001100b)
Register 0xb5 = 0x00 (00000000b)
Register 0xb6 = 0x00 (00000000b)
Register 0xb7 = 0x2f (00101111b)
Register 0xb8 = 0xcc (11001100b)
Register 0xb9 = 0x00 (00000000b)
Register 0xba = 0x00 (00000000b)
Register 0xbb = 0xa5 (10100101b)
Register 0xbc = 0x50 (01010000b)
Register 0xbd = 0x87 (10001111b)

Register 0xbe = 0x2e (00101110b)
Register 0xbf = 0x02 (00000010b)
Register 0xc0 = 0x3c (00111100b)
Register 0xc1 = 0x8a (10001010b)
Register 0xc2 = 0xc9 (11001001b)
Register 0xc3 = 0x87 (10000111b)
Register 0xc4 = 0x07 (00000111b)
Register 0xc5 = 0x55 (01010101b)
Register 0xc6 = 0xcc (11001100b)
Register 0xc7 = 0x00 (00000000b)
Register 0xc8 = 0x00 (00000000b)
Register 0xc9 = 0xcc (11001100b)
Register 0xca = 0x00 (00000000b)
Register 0xcb = 0x00 (00000000b)
Register 0xcc = 0x05 (00000101b)
Register 0xcd = 0xcc (11001100b)
Register 0xce = 0x00 (00000000b)
Register 0xcf = 0x00 (00000000b)